

International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering

ISO 3297:2007 Certified

Vol. 5, Issue 6, June 2017

## Analysis of P-Q and D-Q APF using Hysteresis and SPWM Techniques

Shivayogi Talagalamani<sup>1</sup>, Sandhya .P<sup>2</sup>

M.Tech Student (Power Electronics), Dept of EEE, The Oxford College of Engineering Bengaluru, Karnataka, India<sup>1</sup>

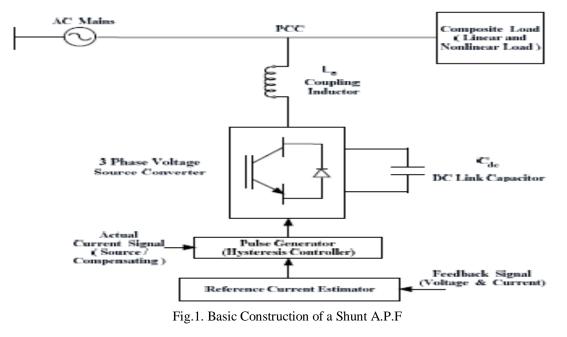
Associate Professor, Department of EEE, The Oxford College of Engineering Bengaluru, Karnataka, India<sup>2</sup>

**Abstract**: By the high-quality usage of power electronics devices which include rectifier, inverter, U.P.S, S.M.P.S, and speed drives and so on, many problems are introduced in power systems. One of such trouble is generation of current& voltage harmonics inflicting distortion of load waveform, voltage variation, voltage depression, heating of system and many others. This paper is an attempt to mitigate the harmonics by using different control method for the Shunt A.P.F. The different control techniques used in the proposed performance analysis are the d-q method using PI controller with current hysteresis controller and PI controller with SPWM controller. The other set of controller technique is the p-q method using PI controller with current hysteresis controller and PI controller with SPWM controller. The simulation of proposed circuit is done by using MATLAB& the results are tabulated and the performance evaluation is carried out.

Keywords: Shunt active power filter (SAPF), P-Q method, D-Q method, Current control, Total harmonics distortion.

## I. INTRODUCTION

Power electronic switching device at the side of nonlinear loads reasons severe harmonic trouble in electricity tool. Main consequences of harmonics are over-heating, capacitor failure, resonance trouble, small energy component, overcapacity, communication interference and energy instability [1-2]. Therefore to enhance the overall performance, it is essential to remove harmonics from energy application device. One of the technique used for harmonics removal is using S.A.P.F, wherein a reference current is produced. S.A.P.F may be used with different current controller scheme together with D-Q technique, fuzzy control, P-Q technique, neural systems and so on[3-7], which are beneficial in eliminating powerful harmonic from energy device.



### II. DIFFERENT CONTROLLING STRATEGIES FOR SHUNT APF

The different control strategies discussed here are p-q method, d-q method, current hysteresis controller and sinusoidal pulse width modulation (SPWM) and PI controller.



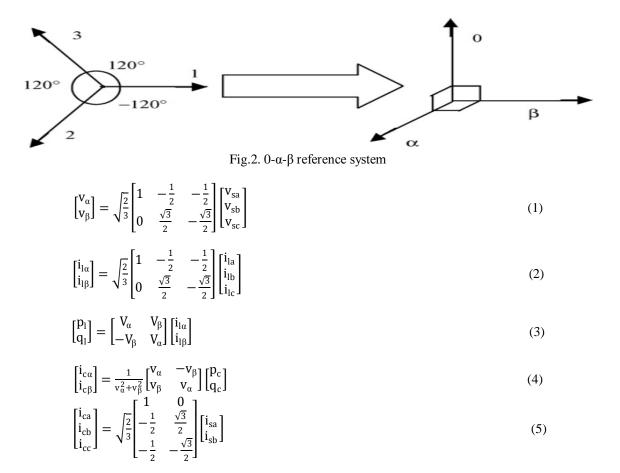
# International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering

ISO 3297:2007 Certified

Vol. 5, Issue 6, June 2017

### A. P-Q Technique

The p-q theory is based on the set of instantaneous power in time domain. 3 phase voltage and current waveforms are transformed from the a-b-c coordinates to  $\alpha$ - $\beta$  coordinates and this theory is based on Clarke transformation.



#### B. D-Q Technique

In D-Q technique, reference currents are produced from the d-q component of the nonlinear load, and this controlling technique is based on park transformation.

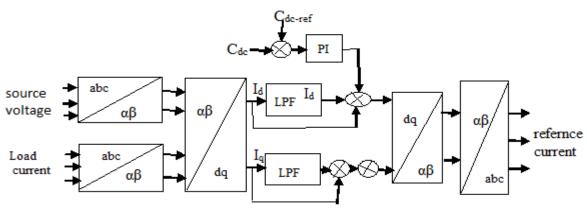


Fig.3. Controlling algorithm using d-q technique

The d-q load current are derived using the Park transformation.

$$\begin{bmatrix} i_{ld} \\ i_{iq} \end{bmatrix} = \frac{1}{\sqrt{\mathbf{v}_{\alpha}^{2} + \mathbf{v}_{\beta}^{2}}} \cdot \begin{bmatrix} \mathbf{v}_{\alpha} & \mathbf{v}_{\beta} \\ -\mathbf{v}_{\beta} & \mathbf{v}_{\alpha} \end{bmatrix} \cdot \begin{bmatrix} i_{l\alpha} \\ i_{l\beta} \end{bmatrix}$$
(1)



# International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering

ISO 3297:2007 Certified

Vol. 5, Issue 6, June 2017

(2)

#### C. Hysteresis Controller

It is used for producing the gating pulses for the converter. Fig 4 shows the hysteresis controller.

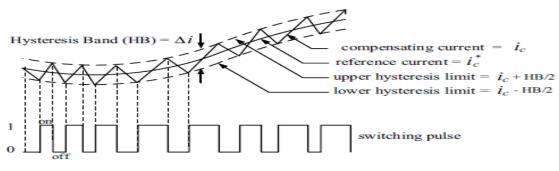
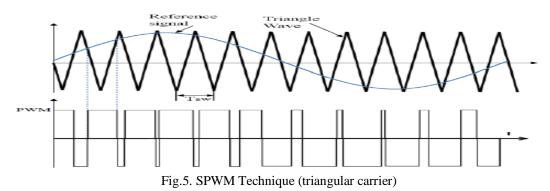


Fig.4. Hysteresis current controller

*D.* Sinusoidal Pulse width Modulation(SPWM)

Triangular carrier (TC): It compares the error within the output current with the fixed value and the fixed triangular wave.



*E.* PI Controller

The figure 6 shows the construction PI controller. The circuit consists of P.I controller, ref. current& hysteresis controller to produce the gating pulses.

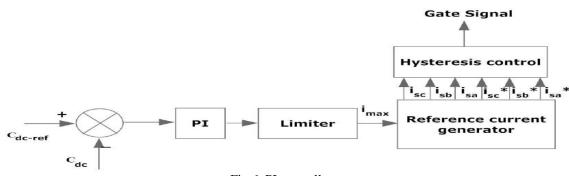


Fig.6. PI controller

### III. SIMULATION RESULTS AND DISSCUSSION

To prove the cogency of the p-q and d-q techniques A.P.F using hysteresis & spwm method, a well-known MATLAB/Simulink tool is used to show the results [8]. The circuit specification are given in table 1.



•

## International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering

ISO 3297:2007 Certified

**IJIREEICE** 

Vol. 5, Issue 6, June 2017

Table.1 S.A.P.F parameter specification

A.P.F Parameters

Coupling Inductance	9Mh
Coupling Resistance	1Ω
DC link Capacitance	1020µF
Source inductance	0.1mH
Source resistance	1Ω
Load resistance	lkΩ
Load inductance	2Mh

### A. Simulation circuit without filter

Figure 7.Shows the non-linear load without compensation.

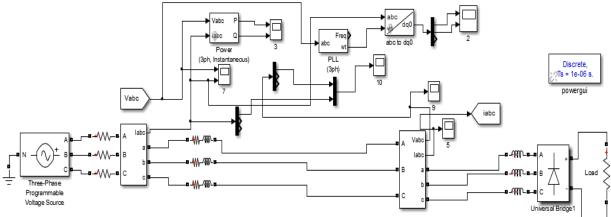
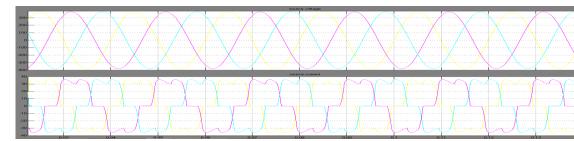
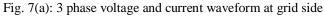
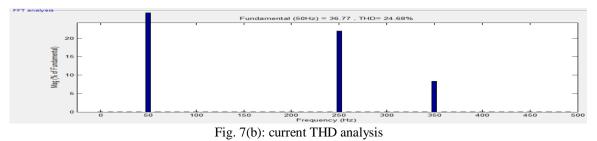


Fig. 7: Simulation circuit of the nonlinear load without compensation







B. Simulation circuit with compensation using Current Hysteresis and PI with d-q technique

The hysteresis controller will compare the reference current with actual current and if the difference is positive the positive sequence switch will be on and similarly for the negative. The fig.8 shows the complete simulation circuit diagram.



(A)(i) Simulated output waveforms



## International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering ISO 3297:2007 Certified

Vol. 5, Issue 6, June 2017

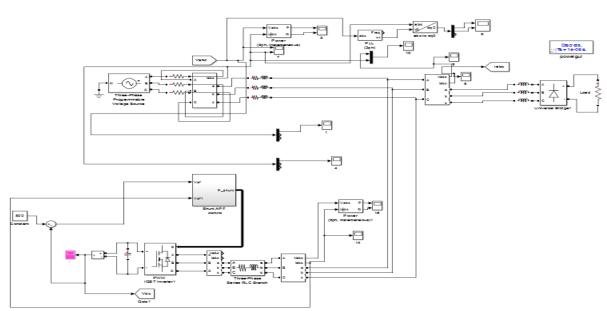


Fig. 8: Simulation circuit of current hysteresis and PI using d-q technique

(B)(i) Simulated output waveforms

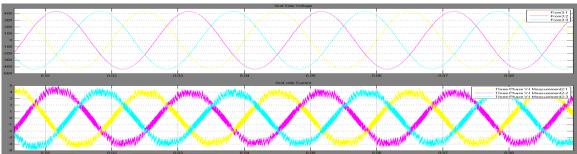


Fig. 8(a): 3 phase voltage and current waveform at grid side

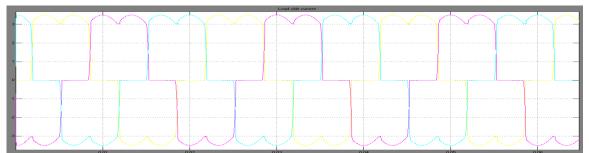


Fig. 8(b): Load current waveform

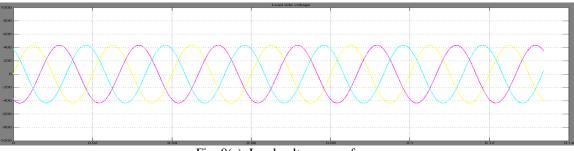


Fig. 8(c): Load voltage waveform



# International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering ISO 3297:2007 Certified Vol. 5, Issue 6, June 2017 Fig. 8(d): compensating current waveform Fundamental (50Hz) = 1.85 , THD= 4.02% Mag (% of Fundame

C. Simulation circuit with compensation using SPWM and PI with d-q technique

SPWM will work with variation in the modulation index of the sinusoidal wave which is compared with triangular wave for generating pulses. The fig.9 shows the complete simulation circuit diagram

Fig. 8(e) current THD analysis

0 250 Frequency (Hz)

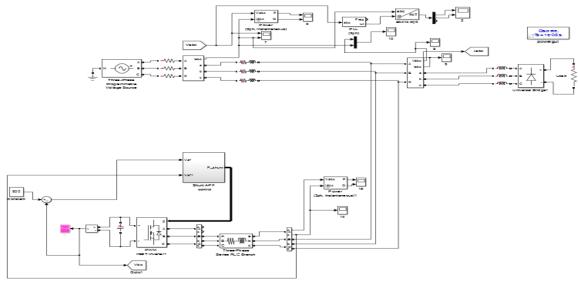


Fig. 9: Simulation circuit of SPWM and PI using d-q technique

(C)(i) Simulated output waveforms

n

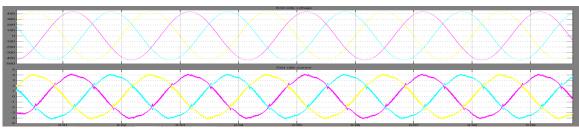
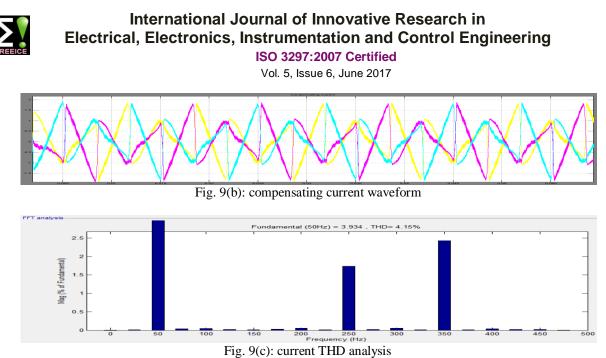


Fig. 9(a): 3 phase voltage and current waveform at grid side



D. Simulation circuit with compensation using current hysteresis and PI with p-q technique The compensating current will generate a pure sinusoidal wave in the source side. The fig.10 shows the complete simulation circuit diagram.

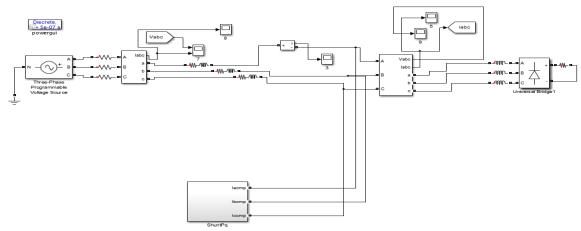


Fig. 10: Simulation circuit of current hysteresis and PI using p-q technique

(D)(i) Simulated output waveforms

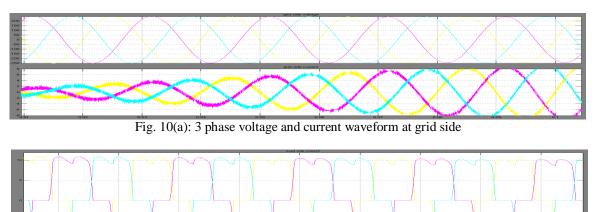
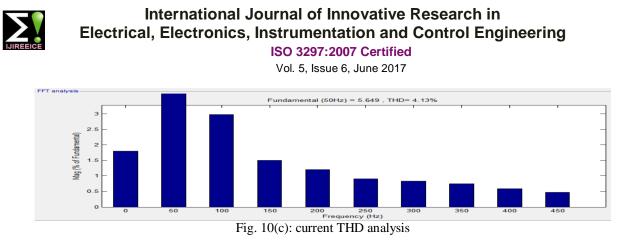


Fig. 10(b): Load current waveform



E. Simulation circuit with compensation using SPWM and PI with p-q technique

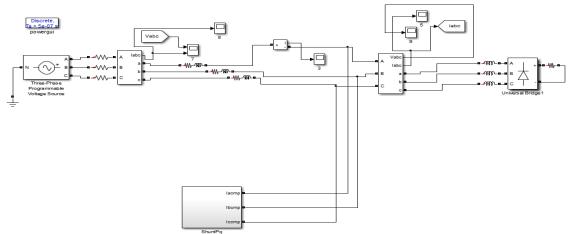
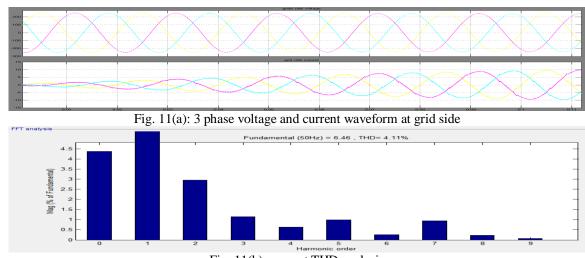
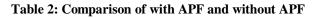


Fig. 11: Simulation circuit of SPWM and PI using p-q technique



### (E)(i) Simulated output waveforms

Fig. 11(b): current THD analysis



METHODS	%THD
D-Q Method With Current Hysteresis	4.02%
D-Q Method With SPWM	4.15%
P-Q Method With Current Hysteresis	4.13%
P-Q Method With SPWM	4.11%
Without A.P.F	24.68%



## International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering

ISO 3297:2007 Certified

Vol. 5, Issue 6, June 2017

## **IV.CONCLUSION**

The THD analysis of different A.P.F methods using p-q and d-q techniques were carried out using the MATLAB tools. The MATLAB Simulink based simulation is developed and tested for reactive power and THD. The THD observed for the proposed system with A.P.F using d-q and p-q method is reduced around 400% from the THD that is observed for the non- compensating power system. It is also observed that the reactive power is also compensated.

#### REFERENCES

- [1] Fuchs, E.F. and Masoum, M.A.S.; "Power Quality in Power System and Electrical Machines," Elsevier, 2009.
- [2] Moreno-Munoz, A.; "Mitigation Technologies in a Distributed Environment," Springer, 2010.
- [3] Singh, B., Chandra, A. and Al-Haddad, K.; "A Review of Active Filters for Power Quality Improvement," IEEE Transactions on Industrial Electronics, Vol. 46, No. 5, pp.1-12, 1999, doi: 10.1109/41.793345.
- [4] Chelladurai, J. ,Ilango, G.S., Nagamani, C. and Kumar, S.S.; "Investigation of Various PWM Techniques for Shunt Active Filter," World Academy of Science, Engineering and Technology, Vol. 2, No.3, pp.173-179, 2008.
- [5] Muthazhagi, M. and Kumar, N.S.; "Comparison of Controllers for Power Quality Improvement Employing Shunt Active Filter," International Conference on Computing, Electronics and Electrical Technologies (ICCEET), pp.248-253, 2012, doi: 10.1109/ICCEET.2012.6203816
- [6] Peng, F.Z. ,Akagi, H. and Nabae, A. ; "Study of Active Power Filters Using Quad Series Voltage Source PWM Converters for Harmonic Compensation," IEEE Transactions on Power Electronics, Vol.5, No.1, pp.9-15, 1990, doi: 10.1109/63.45994.
- [7] Kalaignan, T.P. and Raja, T.S. ; "Harmonic Elimination by Shunt Active Filter Using PI Controller," IEEE International Conference on Computational Intelligence and Computing Research(ICCIC), pp.1-5, 2010, doi: 10.1109/ICCIC.2010.57055800. [8] Jain, S.; "Modelling and Simulation using MaTLAB-Simulink," Wiley-India, 2013.

#### BIOGRAPHIES

Shivayogi Talagalamani completed B.E in EEE from TCE, Gadag in 2015 and pursuing M.tech (Power Electronics) in the Oxford College of Engineering, Bengaluru from 2015-2017. His area of interest are power electronics, power system, power quality, renewable energy, and control system.

Sandhya.P graduated from KVGCE, Sullia, Karnataka, India, in 1997 and obtained M.Tech in power electronics from BMSCE, Bengaluru, India, in 2008. She is currently pursuing Ph.D., under VTU and working as Associate Professor, in the Department of EEE, The Oxford College of Engineering, Bengaluru, since 2001. Her area of interest are power electronics, power system, power quality, and control system.